

TSQ-7S7050



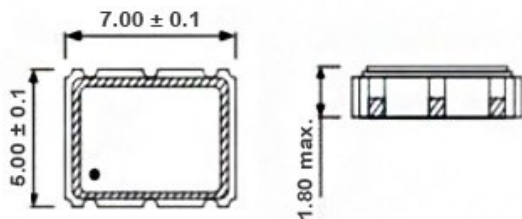
FEATURES

- LVDS LVPECL HCSL
- Wide Frequency Range
- Low Operating Voltage:1.8V/2.5V/3.3V
- Tri-state Available

E L E C T R I C A L S P E C I F I C A T I O N S

Frequency Range	10MHz to 250MHz
Output	LVDS, LVPECL, HCSL
Supply Voltage	1.8V,2.5V,3.3V (±10%)
Frequency Stability	±20ppm
Operating Temperature Range	-40°C ~ +85°C
Current Consumption	100mA max.
Output Load	100Ω between output and complementary output
Symmetry	45% to 55%
Start-up Time	3ms max.
Rise/ Fall Time	1ns max.
Output Enable/ Disable Time	Enable:10ms max. Disable:0.2μs max.
Output Logic High "1"	70%VDD.
Output Logic Low "0"	30%VDD.
Phase Jitter	0.5psec. @12KHz to 20MHz
Storage Temperature Range	-55°C ~ +125°C
Aging (at 25°C)	±2ppm/year max.

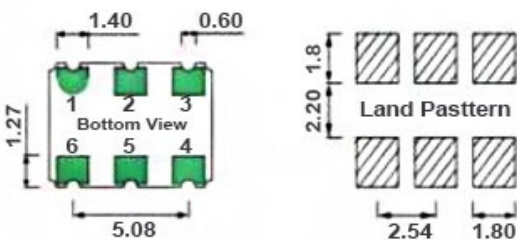
Mechanical Dimensions: Unit: mm



SX7.6 PADS.7.0x5.0x1.8mm

Packing: T&R(1kpcs/reel)

Pin Connections:



#1: E/D

#2: N/C

#3: GND

#4: Output

#5: Complementary output.

#6: Vdd

Note 1: Tolerance ±0.2mm without mark.

Note 2: Referential weight 0.2g

Note: Specifications are subject to change without notice. For more detail and update, please visit our website.